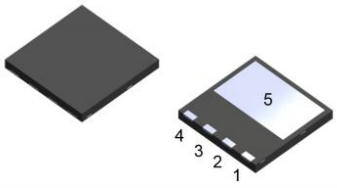
Technical Data
Data Sheet N2628, REV.-

S6D10065L
650V SIC POWER SCHOTTKY RECTIFIER

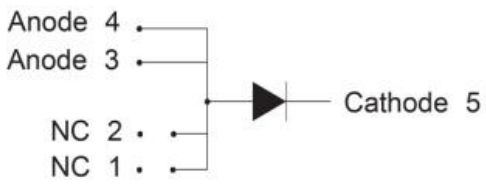


DFN8*8

Description

S6D10065L is a SiC Schottky rectifier packaged in DFN8*8 case. The device is a high voltage Schottky rectifier that has very low total conduction losses and very stable switching characteristics over temperature extremes. The S6D10065L is ideal for energy sensitive, high frequency applications in challenging environments.

Circuit Diagram



Features

- **175°C T_J operation**
- **Ultra-low switching loss**
- **Switching speeds independent of operating temperature**
- **Low total conduction losses**
- **High forward surge current capability**
- **High package isolation voltage**
- **Terminals finish: 100% Pure Tin**
- **"-A" is an AEC-Q101 qualified device**
- **Pb − Free Device**
- **All SMC parts are traceable to the wafer lot**
- **Additional electrical and life testing can be performed upon request**

Applications

- **Alternative energy inverters**
- **Power Factor Correction (PFC)**
- **Free-Wheeling diodes**
- **Switching supply output rectification**
- **Reverse polarity protection**

Maximum Ratings

Characteristics	Symbol	Condition	Max.	Units
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_{DC}	-	650	V
Average Rectified Forward Current	$I_{F\ (AV)1}$	Tc=25°C	33	A
	$I_{F\ (AV)2}$	Tc=135°C	14	A
	$I_{F\ (AV)3}$	Tc=150°C	10	A
Repetitive Peak Forward Surge Current	I_{FRM1}	10ms, Half Sine pulse, T_C=25°C	48	A
	I_{FRM2}	10ms, Half Sine pulse, T_C=110°C	25	A
Peak One Cycle Non-Repetitive Surge Current	I_{FSM1}	10ms, Half Sine pulse, T_C=25°C	80	A
	I_{FSM2}	10ms, Half Sine pulse, T_C=110°C	72	A
Non-Repetitive Peak Forward Surge Current	$I_{F,Max1}$	10μs. Pulse, T_C=25℃	1250	A
	$I_{F,Max2}$	10μs. Pulse, T_C=110℃	1100	A
Power Dissipation	P_{tot1}	T_C=25℃	103	W
	P_{tot2}	T_C=110℃	45	W

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Electrical Characteristics:

Characteristics	Symbol	Condition	Typ.	Max.	Units
Forward Voltage Drop*	V_{F1}	@ 10A, Pulse, T_J = 25 °C	1.35	1.50	V
	V_{F2}	@ 10A, Pulse, T_J = 175 °C	1.5	1.60	V
Reverse Current*	I_{R1}	@V_R = rated V_R T_J = 25 °C	0.7	40	uA
	I_{R2}	@V_R = rated V_R T_J = 175 °C	7	160	uA
Junction Capacitance	C_T	V_R=0V, T_J=25℃, f=1MHz	769	-	pF
Reverse Recovery Charge	Q_c	I_F = 10A, di/dt = 200A/μs V_R = 400 V, T_J =25°C	47.91	-	nC
Capacitance Stored Energy	E_C	V_R = 400 V, T_J =25°C	11.74	-	μJ

* Pulse width < 300 μs, duty cycle < 2%

Thermal-Mechanical Specifications:

Characteristics	Symbol	Condition	Specification	Units
Junction Temperature	T_J	-	-55 to +175	°C
Storage Temperature	T_{stg}	-	-55 to +175	°C
Typical Thermal Resistance Junction to Case	$R_{\theta JC}$	DC operation	1.45	°C/W

Marking Diagram

Where XXXXX is YYWWL

S6D = Device Type
L = Package type
10 = Forward Current (10A)
065 = Reverse Voltage (650V)
SSG = SSG
YY = Year
WW = Week
L = Lot Number

Cautions: Molding resin
Epoxy resin UL:94V-0

Ordering Information

Device	Package	Shipping
S6D10065L	DFN 8*8	3000Pcs/Reel
S6D10065LTR	DFN 8*8	3000Pcs/Reel

For information on tape and reel specifications, including part orientation and tape sizes, please refer to our tape and reel packaging specification.

Ratings and Characteristics Curves

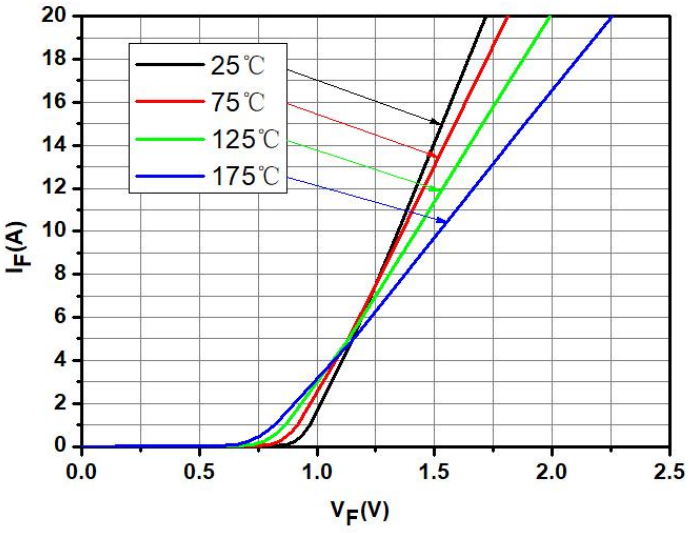

Fig.1-Typical Forward Voltage Characteristics

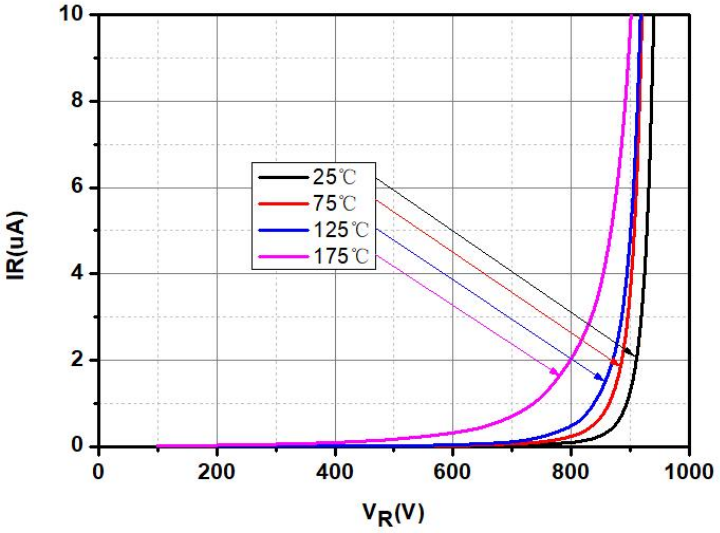

Fig.2-Typical Reverse Characteristics

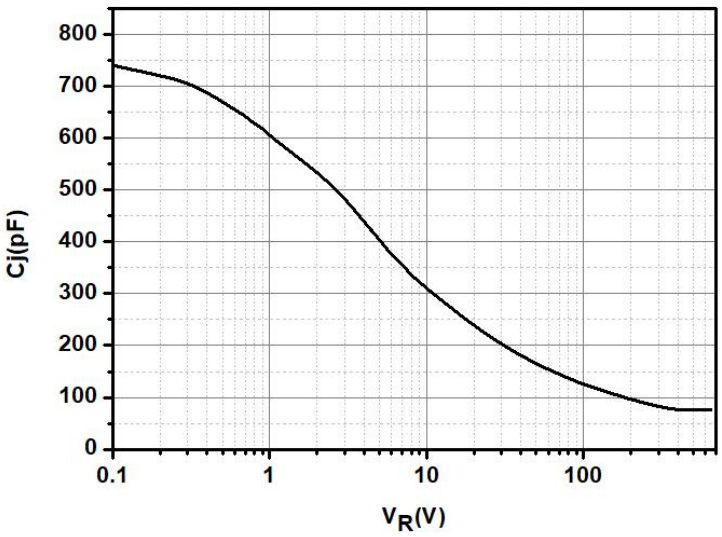

Fig.3-Capacitance vs. Reverse Voltage

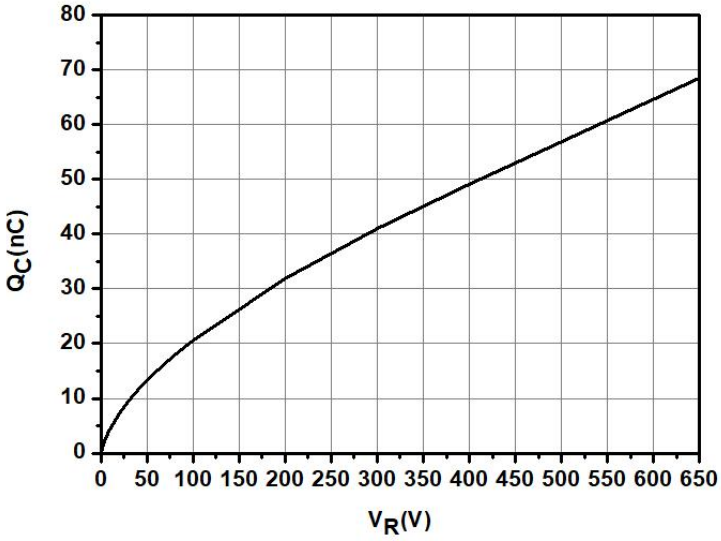

Fig.4-Total Capacitance Charge vs. Reverse Voltage

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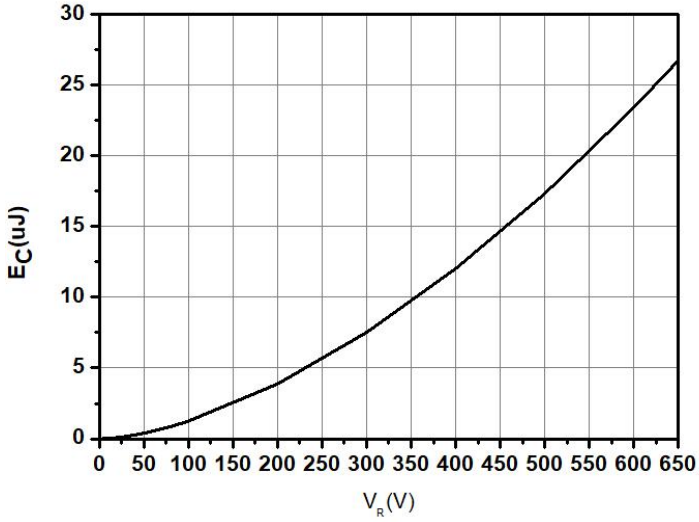


Fig.5-Capacitance Stored Energy

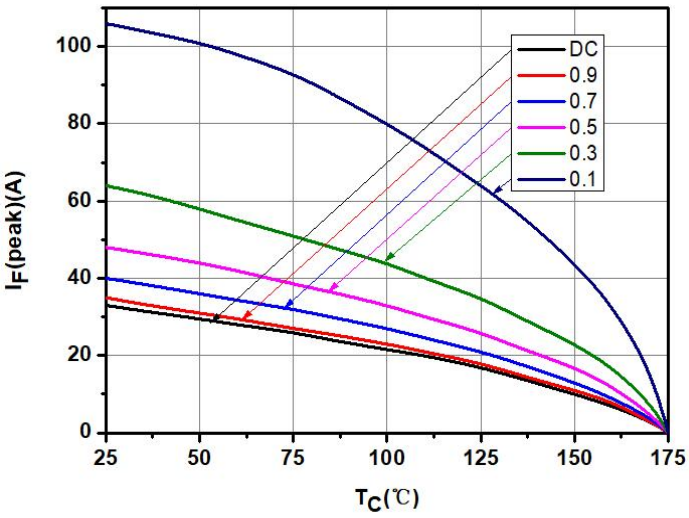


Fig.6-Current Derating

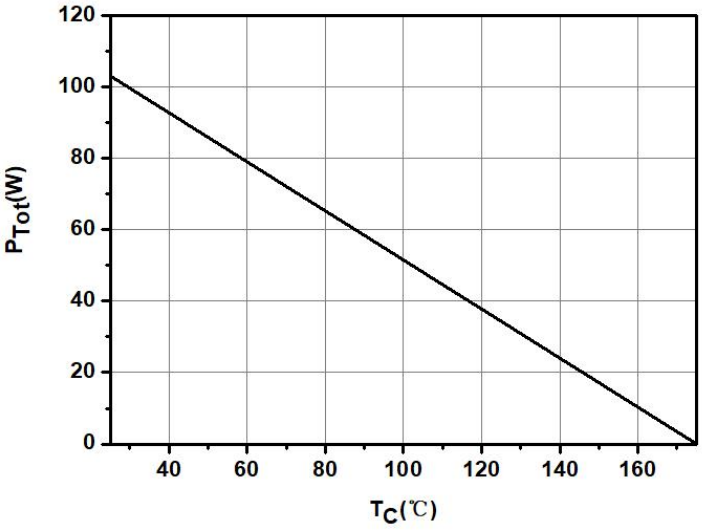
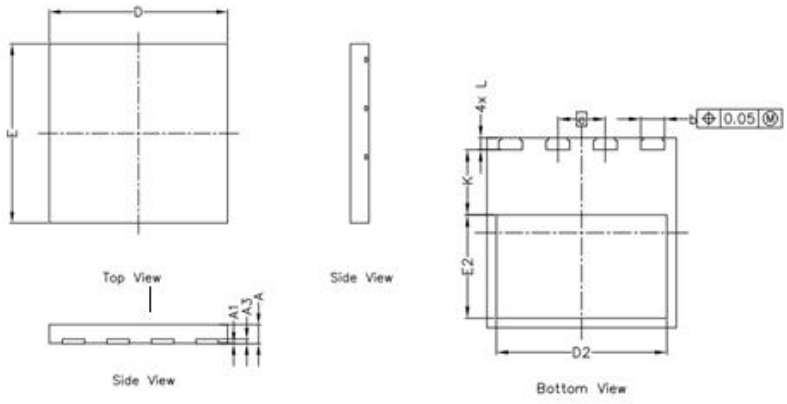


Fig.7-Power Derating

Mechanical Dimensions DFN8*8



SYMBOL	Millimeters	
	Min.	Max.
A	0.800	0.900
A1	-	0.050
A3	0.195	0.211
D	7.900	8.100
E	7.900	8.100
e	2.00 BSC	
b	0.950	1.050
D2	7.100	7.300
E2	4.250	4.450
L	0.400	0.600
K	2.650	2.850

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